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6948039, Data backup and restoration using dynamic **virtual storage**, 2005-09-20 .... and the method further includes maintaining a first list of **pointers** to ...
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Hosts communicate with **physical storage** devices using SCSI protocol ..... backups are in place, as with every migration, to allow crude **data restoration** ...
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**Data restoration**, 487, 540. Data sharing, 205–7, 356 ..... to **virtual storage**, 247. Mirror resynchronization (resilvering),. 457, 487 ...
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The reorganization of the **physical storage** media to store each client's data ..... of media mounts required during client **data restoration**. ...
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A typical index contains two fields: a value and a **pointer** to instances of ..... hashing A way of mapping data records to various **physical storage** areas. ...
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4467421, **Virtual storage** syst... 6996669, Cluster-based cach. .... tasks such as communicating with **physical storage** components and recovering from failure. ...
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is a **physical storage** device. It can be identified by a directory name, ..... differential backups and **data restoration** is mediated by the database. ...
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Terms used: **data restoration virtual storage physical storage pointer**

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1 The logical structure of the memory resource in the symbol-2R computer

Hamilton Richards, Roy J. Zingg
November 1973 **ACM SIGPLAN Notices , Proceedings of a symposium on High-level-language computer architecture SIGPLAN '73**, Volume 8 Issue 11
**Publisher:** ACM Press
Full text available: pdf(762.29 KB) Additional Information: full citation, references

2 The logical structure of the memory resource in the SYMBOL-2R computer

Hamilton Richards, Roy J. Zingg
November 1973 **Proceedings of the ACM-IEEE symposium on High-level-language computer architecture**
**Publisher:** ACM Press
Full text available: pdf(767.75 KB) Additional Information: full citation, abstract, references, citings, index terms

As has been reported elsewhere 1-5, the SYMBOL-2R computer system's basic design premises included the following: 1. implementation of a user-oriented high-level programming language directly in hardware; 2. provision of interactive computing service for as many as 31 independent user terminals simultaneously; 3. incorporation of a virtual-memory system, using a small core memory to buffer a large paging drum. In this paper, w ...

3 Lightweight shared objects in a 64-bit operating system

Jeffrey S. Chase, Henry M. Levy, Edward D. Lazowska, Miche Baker-Harvey
October 1992 **ACM SIGPLAN Notices , conference proceedings on Object-oriented programming systems, languages, and applications OOPSLA '92**, Volume 27 Issue 10
**Publisher:** ACM Press
Full text available: pdf(2.08 MB) Additional Information: full citation, references, citings, index terms

4 Programming without pointer variables

Richard B. Kieburtz
March 1976 **ACM SIGMOD Record , ACM SIGPLAN Notices , Proceedings of the 1976 conference on Data : Abstraction, definition and structure**, Volume 8 , 11 Issue

2 , SI
**Publisher:** ACM Press

Full text available: pdf(1.27 MB) Additional Information: full citation, abstract, references, citings, index terms

The presence of pointer variables in high level programming languages constitutes an artifact originally introduced to support the representation of recursive data structures. Programming practice has come to rely on pointer variables for their originally intended use, and for several others as well. Their use adds to the complexity of stating algorithms by forcing one to conceptualize data representations in which storage addressing is made manifest. In addition, the use of pointer variabl ...

5 Logical, internal, and physical reference behavior in CODASYL database systems

Wolfgang Effelsberg, Mary E. S. Loomis
June 1984 **ACM Transactions on Database Systems (TODS)**, Volume 9 Issue 2
**Publisher:** ACM Press

Full text available: pdf(1.77 MB) Additional Information: full citation, abstract, references, citings, index terms, review

This work investigates one aspect of the performance of CODASYL database systems: the data reference behavior. We introduce a model of database traversals at three levels: the logical, internal, and physical levels. The mapping between the logical and internal levels is defined by the internal schema, whereas the mapping between the internal and the physical levels depends on cluster properties of the database. Our model explains the physical reference behavior for a given sequence of DML s ...

6 Data management requirements: The similarity of memory management, database systems, and message processing

Olin H. Bray
January 1977 **ACM SIGIR Forum , ACM SIGARCH Computer Architecture News , ACM SIGMOD Record , Proceedings of the 3rd workshop on Computer architecture : Non-numeric processing**, Volume 12 , 6 , 9 Issue 1 , 2 , 2
**Publisher:** ACM Press
Full text available: pdf(927.65 KB) Additional Information: full citation, abstract, references, index terms

Memory management, database management, and message processing have in the past been defined in a relatively narrow way. With memory management the problem was to obtain cost effective use of real memory. Given a multiprogrammed environment, virtual memory systems allowed more effective use of expensive real memory. Memory management has become even more important with the development of very large and complex memory hierarchies. Database management systems were developed to allow the more ...

7 Sharing and protection in a single-address-space operating system

Jeffrey S. Chase, Henry M. Levy, Michael J. Feeley, Edward D. Lazowska
November 1994 **ACM Transactions on Computer Systems (TOCS)**, Volume 12 Issue 4
**Publisher:** ACM Press

Full text available: pdf(2.87 MB) Additional Information: full citation, abstract, references, citings, index terms

This article explores memory sharing and protection support in Opal, a single-address-space operating system designed for wide-address (64-bit) architectures. Opal threads execute within protection domains in a single shared virtual address space. Sharing is simplified, because addresses are context independent. There is no loss of protection, because addressability and access are independent; the right to access a segment is determined by the protection domain in which a thread executes. T ...

**Keywords**: 64-bit architectures, capability-based systems, microkernel operating

systems, object-oriented database systems, persistent storage, protection, single-address-space operating systems, wide-address architectures

**8** A very easy hierarchical DBMS implementation

Tamira Bonar, James Driscoll
January 1979 **Proceedings of the 1979 annual conference ACM 79**
**Publisher:** ACM Press
Full text available: pdf(570.97 KB) Additional Information: full citation, abstract, references, index terms

The implementation of a DBMS offering a hierarchical view is described. The implementation follows a unique architecture designed to simplify DBMS implementation. The architecture incorporates basic physical storage constructs for specifying actual data storage structure, and primitive physical navigation operations for the purpose of implementing data manipulation commands. A brief discussion of the overall architecture, the physical storage language, and the physical navigation language a ...

**Keywords**: Atom sets, DBMS architecture, Data manipulation, FAN link, Hierarchical DBMS, Link forms, Physical navigation language, Physical storage language, REC link, Storage structure, Universal DBMS

**9** Database Reorganization—Principles and Practice

Gary H. Sockut, Robert P. Goldberg
December 1979 **ACM Computing Surveys (CSUR)**, Volume 11 Issue 4
**Publisher:** ACM Press
Full text available: pdf(1.89 MB) Additional Information: full citation, references, citings, index terms

**10** A software implemented memory manager

John L. Callaghan
March 1984 **ACM SIGPLAN Notices**, Volume 19 Issue 3
**Publisher:** ACM Press
Full text available: pdf(540.63 KB) Additional Information: full citation, references

**11** Virtual memory implementation: The multics virtual memory

A. Bensoussan, C. T. Clingen, R. C. Daley
October 1969 **Proceedings of the second symposium on Operating systems principles SOSP '69**
**Publisher:** ACM Press
Full text available: pdf(1.11 MB) Additional Information: full citation, abstract, references, citings

As experience with use of on-line operating systems has grown, the need to share information among system users has become increasingly apparent. Many contemporary systems permit some degree of sharing. Usually, sharing is accomplished by allowing several users to share data via input and output of information stored in files kept in secondary storage. Through the use of segmentation, however, Multics provides direct hardware addressing by user and system programs of all information, independent ...

**12** The Multics virtual memory: concepts and design

A. Bensoussan, C. T. Clingen, R. C. Daley
May 1972 **Communications of the ACM**, Volume 15 Issue 5
**Publisher:** ACM Press

Full text available: pdf(1.14 MB) Additional Information: full citation, abstract, references, citings

As experience with use of on-line operating systems has grown, the need to share information among system users has become increasingly apparent. Many contemporary systems permit some degree of sharing. Usually, sharing is accomplished by allowing several users to share data via input and output of information stored in files kept in secondary storage. Through the use of segmentation, however, Multics provides direct hardware addressing by user and system programs of all information, indepe ...

**Keywords**: Multics, information sharing, memory hierarchy, memory management, operating system, paging, segmentation, virtual memory

**13** Sequentiality and prefetching in database systems

Alan Jay Smith
September 1978 **ACM Transactions on Database Systems (TODS)**, Volume 3 Issue 3
**Publisher:** ACM Press

Full text available: pdf(1.74 MB) Additional Information: full citation, abstract, references, citings, index terms

Sequentiality of access is an inherent characteristic of many database systems. We use this observation to develop an algorithm which selectively prefetches data blocks ahead of the point of reference. The number of blocks prefetched is chosen by using the empirical run length distribution and conditioning on the observed number of sequential block references immediately preceding reference to the current block. The optimal number of blocks to prefetch is estimated as a function of a number ...

**Keywords**: IMS, buffer management, database systems, dynamic programming, paging, prefetching, sequentiality

**14** Design and implementation of a relational database on a minicomputer

Y. E. Lien
January 1977 **Proceedings of the 1977 annual conference ACM '77**
**Publisher:** ACM Press

Full text available: pdf(565.27 KB) Additional Information: full citation, abstract, references, citings, index terms

DB85, a relational database management system on a minicomputer Interdata 85, is described. It is a single user system to run on a computer with 64K bytes of memory and disk storage. The system supports a high level, relation-algebraic query language which provides facilities for users to define, create, manipulate, and interrogate the relations in the database. Emphasis in the presentation is on the query language design, physical structures of relations, and minicomputer related issues. T ...

**15** DELIS: A decision support system generator for frequency data

Richard W. Butterworth, Robert A. Stephan
May 1986 **ACM SIGAPL APL Quote Quad , Proceedings of the international conference on APL APL '86**, Volume 16 Issue 4
**Publisher:** ACM Press

Full text available: pdf(1.10 MB) Additional Information: full citation, abstract, references, index terms

The title of this paper suggests two concepts, namely Decision Support Systems (DSS) and frequency style data, that were built upon by the APL application called "DELIS". DELIS was developed originally for interactive display of certain frequency data arrays, and has evolved over the past 8 years into a comprehensive system for developing a broad class of related applications. Our paper's objective is to explain the title concepts

well enough to give the reader a feel for the DE ...

**16** CiteSeer$^x$: a scalable autonomous scientific digital library

Huajing Li, Isaac G. Councill, Levent Bolelli, Ding Zhou, Yang Song, Wang-Chien Lee, Anand Sivasubramaniam, C. Lee Giles

May 2006 **Proceedings of the 1st international conference on Scalable information systems InfoScale '06**

**Publisher:** ACM Press

Full text available: pdf(229.57 KB) Additional Information: full citation, abstract, references, index terms

CiteSeer is a scientific literature digital library and search engine which automatically crawls and indexes scientific documents in the fields of computer and information science. Since it's inception in 1997 CiteSeer has grown to index over 730,000 documents and serves over 800,000 requests daily, pushing the limits of the current system's capabilities. In addition, CiteSeer's monolithic architecture inconveniences system maintenance and reduces the flexibility of the system in terms of new fe ...

**17** Virtual Memory

Peter J. Denning

September 1970 **ACM Computing Surveys (CSUR)**, Volume 2 Issue 3

**Publisher:** ACM Press

Full text available: pdf(2.63 MB) Additional Information: full citation, references, citings, index terms

**18** Schema analysis for database restructuring

Shamkant B. Navathe

June 1980 **ACM Transactions on Database Systems (TODS)**, Volume 5 Issue 2

**Publisher:** ACM Press

Full text available: pdf(1.83 MB) Additional Information: full citation, abstract, references, citings, index terms

The problem of generalized restructuring of databases has been addressed with two limitations: first, it is assumed that the restructuring user is able to describe the source and target databases in terms of the implicit data model of a particular methodology; second, the restructuring user is faced with the task of judging the scope and applicability of the defined types of restructuring to his database implementation and then of actually specifying his restructuring needs by translating t ...

**Keywords**: data model, data relationships, data semantics, data structure, database, database design, database management systems, database restructuring, graphical representation of data, schema, stored data

**19** Session 9: Using write-once memory for database storage

David Maier

March 1982 **Proceedings of the 1st ACM SIGACT-SIGMOD symposium on Principles of database systems PODS '82**

**Publisher:** ACM Press

Full text available: pdf(593.24 KB) Additional Information: full citation, references, citings

**20** Architecture and implementation of a VLIW supercomputer

Robert P. Colwell, W. Eric Hall, Chandra S. Joshi, David B. Papworth, Paul K. Rodman, James E. Tornes

November 1990 **Proceedings of the 1990 ACM/IEEE conference on Supercomputing Supercomputing '90**

**Publisher:** IEEE Computer Society

Full text available: pdf(1.29 MB) Additional Information: full citation, abstract, references

Very-Long-Instruction-Word (VLIW) computers achieve high performance by exploiting the fine-grain parallelism present in sequential or vectorizable code. Multiflow's /200 and /300 VLIW systems yielded near-supercomputer performance by this means despite the relatively slow (65 nS) clocks. With its much faster clock period (15 nS) and architectural improvements, the new /500 system attains approximately 4-9X the performance of its predecessors.This paper describes the /500 architecture and implem ...

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Relevance scale

**21** The development of an Ada front end for small computers

J. Bundgaard
May 1985 **ACM SIGAda Ada Letters , Proceedings of the 1985 annual ACM SIGAda international conference on Ada SIGAda '85**, Volume V Issue 2
**Publisher:** Cambridge University Press, ACM Press
Full text available: pdf(521.19 KB) Additional Information: full citation, abstract, references, index terms

This paper concerns the design of an Ada front end that was required to be hosted on small computers. The paper discusses the special problems to be solved when implementing a compiler for a large language, like Ada, on machines with limited resources and capabilities, such as 16 bits for storage addressing. We also outline various possible solutions to these problems and describe in some detail the actual solutions applied in the Ada front end developed by Dansk Datamatik Center (DDC) as part o ...

**22** A language machine

Rodnay Zaks
October 1971 **ACM SIGAPL APL Quote Quad , ACM SIGPLAN Notices**, Volume 3 , 6 Issue 2-3 , 10
**Publisher:** ACM
Full text available: pdf(353.56 KB) Additional Information: full citation, references

**23** Graphical input interaction technique (GIIT)

James J. Thomas, Griffith Hamlin
January 1983 **ACM SIGGRAPH Computer Graphics**, Volume 17 Issue 1
**Publisher:** ACM Press
Full text available: pdf(2.34 MB) Additional Information: full citation, abstract, references, citings

The contents of this document are the result of intensive discussions among the workshop participants. The names listed by each section are the discussion leaders and principal editors. Without the dedicated enthusiam from all the participants, the ideas presented could not have been formulated.

**24** Database Management Systems Development in the USSR

A. G. Dale
September 1979 **ACM Computing Surveys (CSUR)**, Volume 11 Issue 3

**Publisher:** ACM Press
Full text available: pdf(1.34 MB) Additional Information: full citation, references, citings, index terms

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Zhiwei Qu; Yan Chen; Zhenhua Zhang; Boon-Lock Yeo;
Mechatronics, 2004. ICM '04. Proceedings of the IEEE International Conferen
3-5 June 2004 Page(s):452 - 458
Digital Object Identifier 10.1109/ICMECH.2004.1364481
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Shilin Xiao; Qingji Zeng; Jianxin Wang; Jie Xu; Yun Wang;
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Volume 15, Issue 4, April 2003 Page(s):605 - 607
Digital Object Identifier 10.1109/LPT.2003.809266
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5. **Adaptive Bayesian multiuser detection for synchronous CDMA with Gau noise**
Xiaodong Wang; Rong Chen;

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Volume 48, Issue 7, July 2000 Page(s):2013 - 2028
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6. **Turbo equalization for GMSK signaling over multipath channels based o**
Yang, Z.; Wang, X.;
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7. **Missing sensor data restoration for vibration sensors on a jet aircraft en**
Narayanan, S.; Vian, J.L.; Choi, J.J.; Marks, R.J., II; El-Sharkawi, M.A.; Thom
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12. **Access to DIII-D data located in multiple files and multiple locations**
McHarg, B.B., Jr.;
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Rakers, P.; Connell, L.; Collins, T.; Russell, D.;
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Krissian, K.;
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